



Architecture and Model for VPLS Bandwidth Management for VPLS

(draft-augustyn-vpls-arch-00.txt)
(draft-augustyn-vpls-bw-00.txt)

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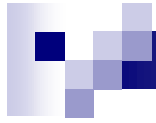
Overview, Issues

- Roadmap for VPLS solution work
 - Diligent review of functional components
 - Proper support for redundant networks
- Possible integration of various arch docs
 - Do we need a focused arch doc for VPLS?
- Adding another dimension to connectivity
 - Bandwidth the most prominent element



Future Steps

- Possible integration with other L2 arch docs to form a single L2 architecture
- Working doc for the L2 solution design team
- Continuing investigation of proper bandwidth control for VPLS
- Comments needed
- Extending beyond bw to delay, jitter, etc.



Q&A