

# ITU-T Q13/15 Updates

TICTOC / IETF-88

Jean-Loup Ferrant, Calnex Solutions , Q13/15 Rapporteur

Stefano RUffini, Ericsson, Q13/15 Associate Rapporteur

# Summary

---

- › Q13/15 met for an Interim meeting in October 2013 (hosted by Sprint)
- › Most of the document consented July were finally approved;
  - some of these after some minor adjustments (G.8272 Amd1, G.8260 Amd 1, New G.8271.1, Rev G.8261, G.8263 Amd1).
  - The G.8275 (Time sync architecture) required an Additional Review (to be completed on the 21<sup>st</sup> of Nov).
- › Some adjustments to the G.8275.1 draft (IEEE1588 time sync profile with full timing support);
  - expected to be consented in April 2014
- › Progress with the Boundary Clock specification (G8273.2).
  - Second clock type (with more stringent static noise characteristics, 20 ns)
  - Adding specification to limit noise due to SyncE rearrangements (when PTP is combined with SyncE)
  - Expected to be consented in April 2014
- › Progress also on PDV tolerance clock testing (G.8263 Amendment?) and possible definition of new PDV network limits (G.8261.1 Amendment?)
- › New work item on Partial Timign Support («Assisted partial timing support», where PTP is used as back up to GPS)

# Current DocUment Structure

