



draft-chairs-6lo-dispatch-iana-registry-03

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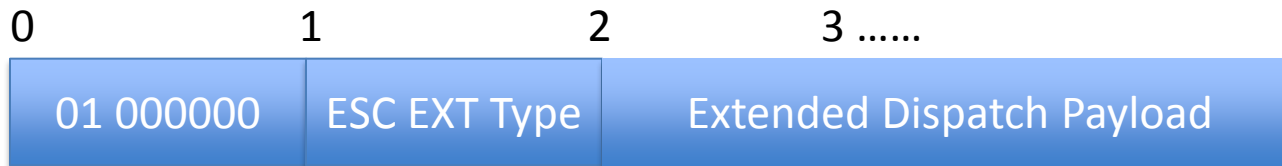
IETF 95, April 7, 2016

Background

- ITU-T G3-PLC reported their use of ESC bytes dispatch header space along with Mesh Header
 - Deployment of their use of ESC bytes
 - ESC byte Dispatch values 1-31 for command frames [No IANA assignments]
 - Thierry Lys – April, 2015
- Liaison Statement sent to ITU-T SG15
 - <https://datatracker.ietf.org/liaison/1425/>
 - During IETF 93, July 2015

Usage of ESC bytes

- ESC EXT Type (EET)
 - ESC dispatch type values are orthogonal to other dispatch values
- Extended payloads must be predefined



ESC EXT Type Values	Description	Reference/Comment
0,255	Reserved for future use	This document
1 - 31	ITU-T use	ITUT-G.9903, ITU-T G.9905
32-254	Unassigned-Reserved for future IANA assignment	This document

Draft Update Status

- Since IETF94
 - Clarified NALP behavior (Pascal, Michael R.)
 - WG Adoption and LC completed
- Major comments addressed:
 - ITU-T comments on editorial fixes [Thierry Lys]
 - ESC byte sequence when LOWPAN_IPHC is present
 - ESC byte before the IPHC dispatch [Pascal Thubert]

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+-----+-----+-----+-----+-----+-----+-----+
| ESC | EET | EDP | Dispatch | LOWPAN_IPHC hdr | Payld |
+-----+-----+-----+-----+-----+-----+-----+
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Next Step

- Ready for IESG submission